

## Wafer-Scale Thermionic Energy Converters

Roger T. Howe  
Dept. of Electrical Engineering  
Stanford University

Thermionic energy converters were conceived in 1915, demonstrated in 1939, and were the focus of very large investments during the Cold War by the U.S. and the Soviet Union for space power applications. A 6 kW thermionic converter, fabricated using precision machining technology, was flown on a reconnaissance satellite in 1987 by the Soviet Union. Over recent decades, research on thermionic converters has lagged, due to the perception that the technology is impractical for commercial applications.

Thermionic converters can be fabricated using processes borrowed from micro electromechanical systems (MEMS). Advances in materials, micromachining, and vacuum encapsulation can be used to enhance performance and reduce manufacturing costs. Potential commercial applications for wafer-scale thermionics include small-scale co-generation and topping cycles for conventional heat engines. Recently, a new conversion concept has been demonstrated at Stanford, in which a semiconductor photocathode replaces the conventional metal cathode. This photon-enhanced thermionic energy (PETE) converter harvests photon energies above the bandgap, as well as broad-spectrum radiation through heating of the photocathode, making it attractive as a high-temperature topping cycle for solar-thermal power stations. Micro- and nano-structured cathodes and anodes and high-temperature materials are also essential to fabricating wafer-scale, cost-effective PETE converters. I will conclude by summarizing the remaining research challenges that must be surmounted in order to bring thermionic and PETE conversions into the mix of energy conversion options.



Roger T. Howe is the William E. Ayer Professor in the Department of Electrical Engineering at Stanford University. He received a B.S. degree in physics from Harvey Mudd College and an M.S. and Ph.D. in electrical engineering from the University of California, Berkeley in 1981 and 1984. After faculty positions at CMU and MIT from 1984-1987, he returned to Berkeley where he was a Professor until 2005. His research group focuses on nano electromechanical system design and fabrication for a variety of applications. He was elected an IEEE Fellow in 1996, was co-recipient of the IEEE Cleo Brunetti Award in 1998, and was elected to the National Academy of Engineering in 2005, and is the co-recipient of the inaugural IEEE Robert Bosch Award in 2015. He co-founded Silicon Clocks in 2004 to commercialize integrated MEMS resonator-based timing products, which was acquired by Silicon Labs in 2010. He is the Faculty Director of the Stanford Nanofabrication Facility and since September 2011, is Director of the U.S. National Nanotechnology Infrastructure Network (NNIN).